

# 1 High-Level Synthesis for FPGA, Part 3: Advanced

Just we should modify the HLS code as follows.

```
void memory_mapped_output(unsigned int *led) {  
#pragma HLS INTERFACE mode=m_axi port=led offset=off  
#pragma HLS INTERFACE mode=ap_ctrl_hs port=return  
  
    *led = 0xAAAA;  
}
```