

Module 8

Xilinx Tools

Xilinx Tools

- Plan Ahead
 - Used to create “User Constraints File” (*.ucf)
- ISim
 - Used to simulate your VHDL design
- Xilinx ISE
 - Used to compile and create a programming file



Additional Tools

- Digilent's Adept software will be used to program the BASYS 2 Board
- This program can be used to program other development boards as well
- The link for this download is located in the references section



Summary

- Xilinx has many different tools to use in the development and production lifecycle of an embedded system using an FPGA
- There are many other tools from Xilinx I just mentioned the ones we will be using